





Approved by AICTE, New Delhi, Affiliated to JNTUK, Kakinada: Accredited by "NBA" for our CSE & ECE and NAAC A+ Grade Pulladigunta (V) Vatticherukuru (M), Guntur (Dist.)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING Faculty Bio Data

Dr.	C.	Ka	rtl	hi	k,
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Professor, Dept. of ECE

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OBJECTIVE:

To be associated with a team of educational professionals who imparts quality education in the field of Electronics & Communication Engineering.

SUMMARY:

- 14 Years of Experience as a Faculty in engineering colleges.
- Ratified as Associate professorat by JNTUH in 2018.
- Ratified as Assistant professor by JNTUA in 2012.
- Worked as HOD from 2012-2016 in Shree Rama Educational Society, Tirupati.
- Excellent working experience in design, develop and maintenance of the departmental labs according to the University syllabus.
- Strong knowledge in design and developmenton image processing (R&D)
- Guided for various Academic Projects in UG & PG levels







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- Organized National Level Fests & Various College LevelEvents.
- Established Departmental Association of students for improving Technical Knowledge,
 Managerial skills etc ...
- GoodCommunication Skills.
- Good People Management Skills.
- International Journals :15
- National Conferences :02

Educational Qualifications

- Ph.D, at Pondicherry University in Dec-2017.
- M Tech in Embedded Systems at Karunya University (2008 to 2010).
- B Tech in Electronics and Communications at JNTU-H (2004-08).

Technical Skills:

- Hardware Description Languages: Verilog HDL and VHDL.
- Circuit Simulation and verification Tools: B2 SPICE, Modelsim and Active HDL.
- Microwind for Layout design.
- Simulation and verification Tools:MATLAB
- FPGA & CFLD Design Flow Tools: Xilinx ISE.
- Prototype Boards: Xilinx (SPARTAN 3E FPGA and CPLD).
- Prototype Boards for Embedded System: 8051 RTOS Development Board







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Domain Knowledge:

- All Communications subjects which are related to ECE
- DIP,DSP,All core subjects in electronics.
- Hardware Description Languages
- Microwave Engineering
- EDA Tools
- Digital Design through verilog HDL
- Testing And Testability
- Embedded System Concepts

Professional Skills:

- Logical and Analytical.
- Ambitious and Self-motivated.
- Realistic and result-oriented.
- Possess excellent teaching skills.

PUBLICATIONS:

International Journals:

- **1.Dr.V.Saminadan**and **C.karthik** "A survey on Drivers drowsiness detection techniques" International Journal on recent & innovation trends in computing and computation with ISSN:: 2231-8169.
- **2.Karthik.c** and A.Bhanuchander 'Design of high speed floting point multiplier unit' International journal of vlsi system design & communication systems with ISSN2277-128X.
- **3.Karthik.c** and S.Yasmin "A butterfly architecture based on binary signed digit representation of floating point"International journal of vlsi system design & communication systems with ISSN2322-0929.







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- **4.Karthik.c** and A.Prakash "Design & Implementation of Heart rate Measurement device using wireless system"International. Journal & Magazine of Engineering, Technology, Management & Research with ISSN: 2348-4345.
- **5.Karthik.c** and K.Hemanth "An implementation of mobile control room environment in Android platform for Industrial Applications" International Journal of Innovative Technologies with ISSN:2321-8665.
- 6. **Karthik.c** and S.Radhika "Secure & Privacy preserving Smart phone based traffic Information System" International Journal of Innovative Technologies & Research with ISSN:3412-3414.
- 7. **Karthik.c** and S.Khadarvali "Face Recognition over FFT-OFDM Computer Networks" International Journal of Applied Engineering Research with ,ISSN:0973-4562.
- 8. **Karthik.c** alotted D.Mounika "Design & Analysis of a low voltage double tail comparator for Flash ADC"International Journal of Scientific & Engineering Research with ISSN:2229-5518.
- 9. **Karthik.c** and A. Kailash "Optimized logic synthesis of reversible PLA" International Journal of Advanced and Innovative Research with ISSN:2349-6002.
- 10..D.Senthiland**Karthik.c** "Development of human tracking control & obstacle avoiding marathoner bot"International Journal of advanced Research in computer Engineering & Technology with ISSN:2321-8665.
- 11. **IS.PriyaandKarthik.c** "Wireless sensor based Energy Conservation via Bluetooth"International. Journal & Magazine of Engineering, Technology, Management & Research With ISSN:2348-4845.
- 12. S.Neelufurand **Karthik.c** "Compression of color Images using Clustering Techniques" International Journal of science & Engineering Research with ISSN:2229-5518.
- 13. V. Nickeland **Karthik.c** "Image Processing Techniques for Contrast Enhancement with poor lighting on social & medical images" International Journal of computer Applications with ISSN:0975-8887.
- 14. **Karthik.c** and D.Niranjan "Image denoising by using Local pixel grouping with PCA & SVD along with JBF & median filters" International Journal of applied Information systems with ISSN:2249-0868.
- 15. **Karthik.c** and A. Althaf "Algorithm for Efficient attendance management on face recognition based approach" International Journal of Computer Science with ISSN::1694-0814.
- 16.**Karthik.c** and D.Sukanya "Mobile Object Recognition with Its Enhancement" International Journal & Magazine of Engineering, Technology, Management and Research with ISSN: 2348-4845.

National Conferences:

- 1. **Karthik.c** and A.Bhanuchander"Technology optimized fixed point bit parallel multiplier for LUT based FPGAS"IEEE Sponsored 2nd International Conference on, Electronics and Communication systems, Karpagam College of Engineering, Coimbatore 19th & 20th March 2015.
- 2. **Karthik.c** and A.Bhanuchander"A New SCPG Technique to reduce Leakage Power in VLSI Logics" *IEEE Sponsored* 9th international Conference on Intelligent Systems and Control (ISCO), Karpagam College of Engineering, Coimbatore, 9th & 10th january-2015.







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SUBJECTS TAUGHT:

UG Level:

- Digital Image Processing(DIP)
- ➤ Microprocessor & Interfacing (MPI)
- Embedded Systems (ES)
- Principles of Communication (POC)
- ➤ Pulse and Digital Circuits (PDC)
- Digital Logic Designs (DLD)
- ➤ Analog Communications (AC)
- Digital Communication(DC)
- ➤ Electronics and Devices Circuits

Personal Profile:

Name : C KARTHIK

Father's name : CH RAJASEKHAR

Date of birth : 25-06-1987

Gender : Male

Marital status : Married

Languages known: English, Telugu

DECLARATION

I hereby declare that the all the information provided above is true to the best of my knowledge.

PLACE: Guntur (Dr.C. Karthik)